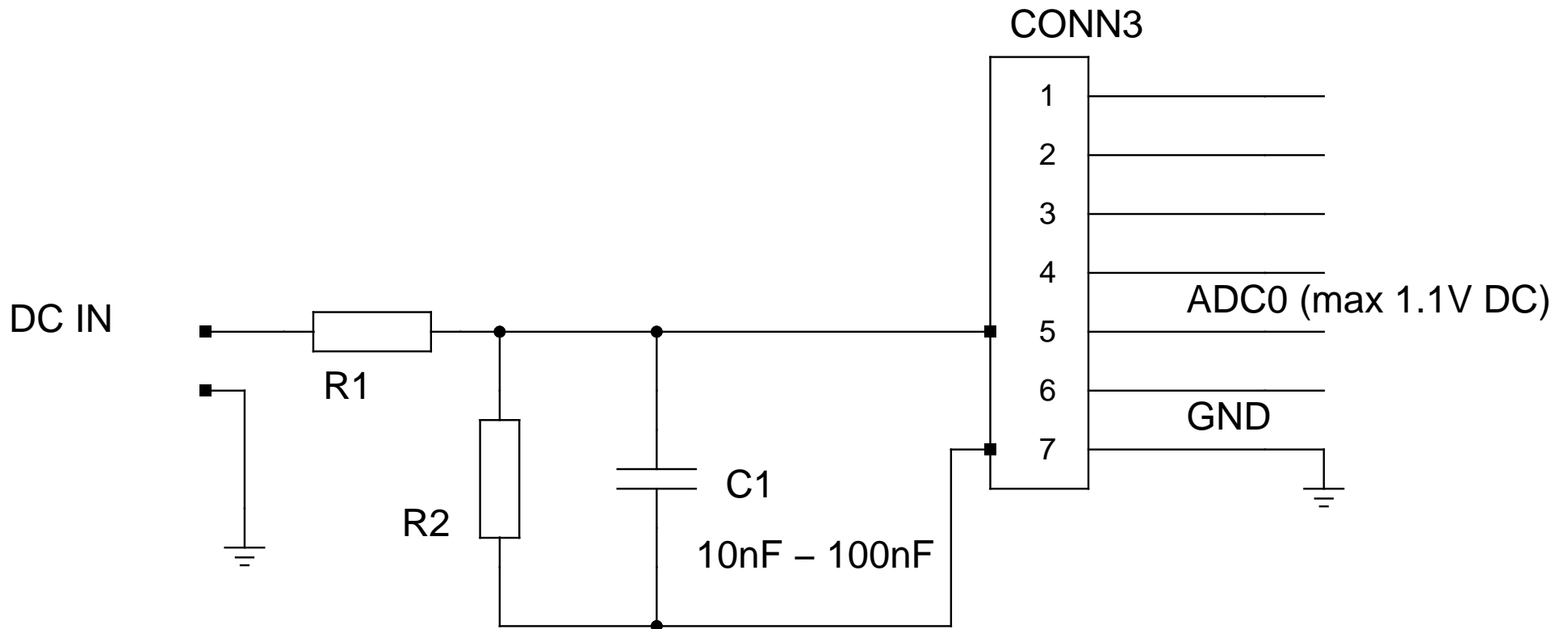


# Voltage divider for the ADC0 input



Recommendation: Dimension R1 and R2 such that:  $100K < (R1+R2) < 700K$